

[illegible]

DISCONNECT DIODE D3 COMMON TERMINAL AND
RECONNECT IT WITH ADDITIONAL JFET TR12 BACK TO TR5.
CONNCT THE TR12 GATE TO THE MIC/LINE CONTROL SIGNAL.
REFER ALSO TO THE ORIGINAL SCHEMATICS

Drawn	2008-04-10	Arch id	Serial no	Sheet	Cooperator's doc no
Review		JUMA TRX2 SSB/CW TRANSCEIVER MAIN BOARD LINE INPUT MODIFICATION			OH7SV
Appr					
Design	OH7SV				
Replaces					
Replaced by					Dwg no
					Rev